

# Synopsys and AMD

AMD's 14nm SoC designs (A12-1000, A12-1000Z, A12-1000ZB) are designed using Synopsys' Design Compiler, Design Vision, and Design Architect.



"Synopsys' Design Compiler, Design Vision, and Design Architect are critical to our SoC designs. Their performance and reliability are essential for our 14nm SoC designs." —Rolands Ezers, Sr. Director, I/O & Circuit Technology, AMD

## Business

AMD's 14nm SoC designs (A12-1000, A12-1000Z, A12-1000ZB) are designed using Synopsys' Design Compiler, Design Vision, and Design Architect.

## Challenges

Designing a 14nm SoC with a high-performance CPU core and a high-performance GPU core is a significant challenge. The design team needed a solution that could handle the complexity of the design and provide the performance and reliability required for the design.

## Synopsys Solution

- Design Compiler
- Design Vision
- Design Architect
- Design Compiler (1000BA12-1000Z, 1000BA12-1000ZB)
- Design Vision
- Design Architect
- Design Compiler (1000BA12-1000Z, 1000BA12-1000ZB)
- Design Vision
- Design Architect

## Benefits

Designing a 14nm SoC with a high-performance CPU core and a high-performance GPU core is a significant challenge. The design team needed a solution that could handle the complexity of the design and provide the performance and reliability required for the design.

