Synopsys and Siglead

Siglead Achieves First-Pass Silicon Success for SSD Controller IC with Synopsys DesignWare IP and ARC Processors

Business

Siglead Inc., a fabless design house with a strong competency in signal processi sgnaspsgsgiy@.56e1(a)-2.18 I-0.05et51.5(n1s)-13yngAchieve high performance and reducintegration effort

Ensure compatibility with end-equipment already in the market

Synopsys Solution

- } DesignWare® IP including:
 - ARC® 625 processor

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High-Quality DesignWare Interface IP

Siglead knew that to meet their aggressive goal of achieving mass production quickly and gaining a large share of the highly competitive SSD controller IC market, they needed to focus their engineering effort on developing and implementing their unique ECC technology. For other critical blocks on their chip, they made the decision to use external IP. After evaluating several IP vendor options based on stringent criteria including quality metrics and a track record of successful silicon, Siglead selected Synopsys, including DesignWare Library, SATA, DDR and AMBA IP. More specifically, Siglead chose Synopsys' SATA 6G IP due to its cutting-edge technology implementation, and chose to add the DDR and AMBA IP for their proven interoperability with standard pro2-Љ(i)-10.6(t)-2Љ(y w)5.5(m)-11.3(e)-16(n)-2(t.5(m)-11.3(e).4(i(o)-10).(£2o-Љ9g2)-9.5(o)(P)125.(7 M)-6(t h)-9.)-10.9riDR arove

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a de iab e o, io . So ... 'e, e i e
De ig Wa e IP o fo io ga e, acce, o he
e i e a ge of IP e eeded, acce e a i go
de e o e i e b a ea e o h a d
e ab i g, o achie e - a i ico , cce.

Yasuyuki Matsumoto

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